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JUN 13 2006

SAWYER LAW GROUP LLP

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FACSIMILE TRANSMITTAL

Date: June 13, 2006

To: Examiner Mathew T. Urick

GROUP ART UNIT: 2113

Organization: United States Patent and Trademark Office

Fax Number: (571) 273-8300

Telephone Number: (571) 272-0805

From: James R. Riegel

Re: USSN 10/609,892

Our Ref.: 2847P

Enclosed is the Applicant Initiated Interview Request Form, along with the Authorization to Act in a Representative Capacity for the above-referenced case.

If you have any questions or need further information, please contact us.

This is page 1 of 3 page(s).

CONFIDENTIALITY NOTE:

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JUN 13 2006

Attorney Docket: RPS920030073US1/2847P

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Date: June 13, 2006

Marcus A. BAKER et al.

Confirmation No.: 8340

Application No.: 10/609,892

Group Art Unit: 2113

Filed: June 30, 2003

Examiner: Urick, Matthew T.

For: METHOD AND SYSTEM FOR SAVING THE STATE OF INTEGRATED

CIRCUITS UPON FAILURE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AUTHORIZATION TO ACT IN A REPRESENTATIVE CAPACITY

The practitioner named below is authorized to conduct interviews and has the authority to bind the principal concerned. Furthermore, the practitioner is authorized to file correspondence in the above-identified application pursuant to 37 CFR 1.34:

James R. Riegel

Reg. No. 36,651

This is not a power of attorney to the above-named practitioner. Accordingly, the practitioner named above does not have authority to sign a request to change the correspondence address, a request for an express abandonment, a disclaimer, a power of attorney, or other document requiring the signature of the applicant, assignee of the entire interest, or an attorney of record.

Respectfully submitted,

SAWYER LAW GROUP LLP

June 13, 2006

Date

Kelvin M. Vivian
Attorney of Record

Reg. No. 47,797 (650) 493-4540

APPLICANT INITIATED INTERVIEW REQUEST FORM

Application No.: 10/609,892		First Nam	First Named Applicant: Baker, Marcus A.			
Examiner: Urick, Matthew T.		Art Unit:	Art Unit: 2113 Status of Application: pending			
Tentative Participants: 1) James Riegel 3)		2) Exami 4)	2) Examiner Urick4)			
Proposed Date of In	Proposed	Proposed Time: 2:00 pm EST				
Type of Interview R (1) [X] Telephonic	equested:	Personal	nal (3)[] Video Conference			
Exhibit To Be Show If yes, provide brief		ted: [] YES	[X] NO			
		ISSUES TO BE	E DISCUSSED			
Issues (Rej., Obj., etc.)	Claims Fig. #s	Prior Art	Discussed	Agreed	Not Agreed	
(1) <u>102(b) rej.</u>	1, 14, 27	Vachon '404	[]	[]	[]	
(2) <u>103(a) rej.</u>	3,9,11,16,24,29,36	Vachon '404, '348	[]	[]	[]	
(3) 103(a) rej.	7,12,22,34	Vachon '404, Masuv	ama []	[]	[]	
(4)			. []	[]	[]	
[] Continuation Sh	eet Attached					
memory when an unrec stoppedFor 102 Rejection, Va an integrated circuit; an integrated circuits are neeror.	nending the indeper overable error occu- tehon '404 does not do the system is still ot stopped. Also, th	ndent claims to include irs in the integrated ci disclose or suggest th I running after failure, e operating system car	is. Vachon disclose, allowing the debug resume execution, a state is close saving a state is close is close saving a state is close is close is close is close in the close is	s an operating sy ger operation, so and so has not ex	nit is saved to a non-volatile integrated circuit have been stem failure, not a failure of a that internal clocks of any operienced an unrecoverable decircuit after unrecoverable to references show operating	
error of the integrated c system failure, continue	ircuit and when inte	rnal clocks of the inter	grated circuit have of	en stopped; mes	of telefetices show operating	
An interview was co	onducted on the	above-identified ap	oplication on	<u> </u>		
MDED 212 (11)	or he deleved from	n issue because of A	oplicant's failure to	submit a writte	en record of this interview. as soon as possible.	
(Applicant/Applicar	nt's Representati	ve Signature)	(Examiner/SPI	E Signature)		